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PTO/SB/08A (08-00)

Approved for use through 10/31/2002. UMB 0651-0031

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	Based on 09/640,077
				Filing Date	August 17, 2000
				First Named Inventor	Ritsuko KAWASAKI, et al.
				Group Art Unit	2813
				Examiner Name	Laura Schillinger
Sheet 1 of 2	Attorney Docket Number 0756-7181				

U.S. PATENT DOCUMENTS						
Examiner Initials ¹	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
AS		5,923,962		Ohtani et al.	07/13/99	
AS		5,643,826		Ohtani et al.	07/01/97	

FOREIGN PATENT DOCUMENTS								
Examiner Initials ¹	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁴
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
AS		JP	07-130652			05/19/95		Abst.

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
AS		K. Shimizu et al., "High-Mobility Poly-Si Thin-Film Transistors Fabricated by a Novel Excimer Laser Crystallization Method," IEEE Transactions on Electron Devices, Vol. 40, No. 1, pp. 112-117, 1993.		
		Ishihara et al., "Location-Controlled Adjacent Grains Following Excimer-Laser Melting of Si Thin-Films," 1998, pp. 153-156, AM-LCD.		
		Specifications and Drawings for Application Serial No. 09/696,165, "Semiconductor Device and Method of Manufacturing the Same," Filed on 10/26/00, pp. 1-63, 33 pages of drawings, Inventors: Ritsuko KAWASAKI, et al.		
		Specifications and Drawings for Application Serial No. 09/598,827, "Method of Fabricating a Semiconductor Device," Filed on 06/21/00, pp. 1-64, 25 pages of drawings, Inventor: Kenji KASAHARA.		
		Specifications and Drawings for Application Serial No. 09/732,724, "Semiconductor Device and A Method of Manufacturing the Same," Filed on 12/11/00, pp. 1-50, 26 pages of drawings, Inventors: Ritsuko KAWASAKI et al.		
		Ishihara et al., "Location Control of Large Grain Following Excimer-Laser Melting of Si Thin-Films," March 1998, pp. 1071-1075, Japanese Journal of Applied Physics, Vol. 37, Pt. 1, No. 3B.		

Examiner Signature	AS	Date Considered	3/7/05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Sheet	2	of	2	Attorney Docket Number	0756-7181

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		Number	Kind Code ² (if known)			
AS ↓ ✓		5,893,730		Yamazaki et al.	04/13/99	
		6,078,070		Robinson	06/20/00	
		6,107,107		Bruce et al.	08/22/00	
		6,245,602		Ho et al.	06/12/01	
		5,811,322		Robinson	09/22/98	
		5,841,197		Adamic Jr.	11/24/98	
		6,346,438		Yagishita et al.	02/12/02	
		6,285,042		Ohtani et al.	09/04/01	
		6,335,451		Ohtani et al.	01/01/02	
		6,492,659		Yamazaki et al.	12/10/02	

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		Office ³	Number ⁴	Kind Code ⁵ (if known)			
AS		JP	2001-203360			07/27/01	Full

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